

WHAT IS CLAIMED IS:

1. A method for adjusting the external clock of a central processing unit (CPU), the CPU equipped in a computer system, the computer system at least comprising an external-clock storage device and a south bridge circuit,

the method comprising steps of:

setting an external-clock value and storing the external-clock value into the external-clock device;

starting an external-clock altering procedure and turning off the computer system;

waking up the south bridge circuit in a wake-up time;

rebooting the computer system; and

providing the central processing unit with the external clock according to the external-clock value stored in the external-clock storage device.

2. The method according to claim 1, wherein the computer system is a notebook computer.

3. The method according to claim 1, wherein the computer system is a desktop computer.

4. The method according to claim 1, wherein the wake-up time is about

one second.

5. The method according to claim 1, wherein the external-clock storage device comprises a plurality of registers.

6. A circuit capable of adjusting the external clock of a CPU equipped in a computer system, comprising:

a keyboard controller for setting an external-clock value of the CPU;

an external-clock storage device coupled to the keyboard controller for storing the external-clock value;

a south bridge circuit for starting an external-clock altering procedure , turning off and turning on the computer system;

a wake-up circuit coupled to the south bridge circuit for waking up the south bridge circuit in a wake-up time after turning off the computer system; and

a clock generator coupled to the external-clock storage device for providing the central processing unit with the external clock according to the external-clock value stored in the external-clock storage device.

7. The circuit according to claim 6, wherein the computer system is a notebook computer.

8. The circuit according to claim 6, wherein the computer system is a desktop computer.

9. The circuit according to claim 6, wherein the wake-up time is about one second.

5 10. The circuit according to claim 6, wherein the external-clock storage device comprises a plurality of registers.

11. The circuit according to claim 6, wherein the wake-up circuit comprises resistors and capacitors (RC) circuit.

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